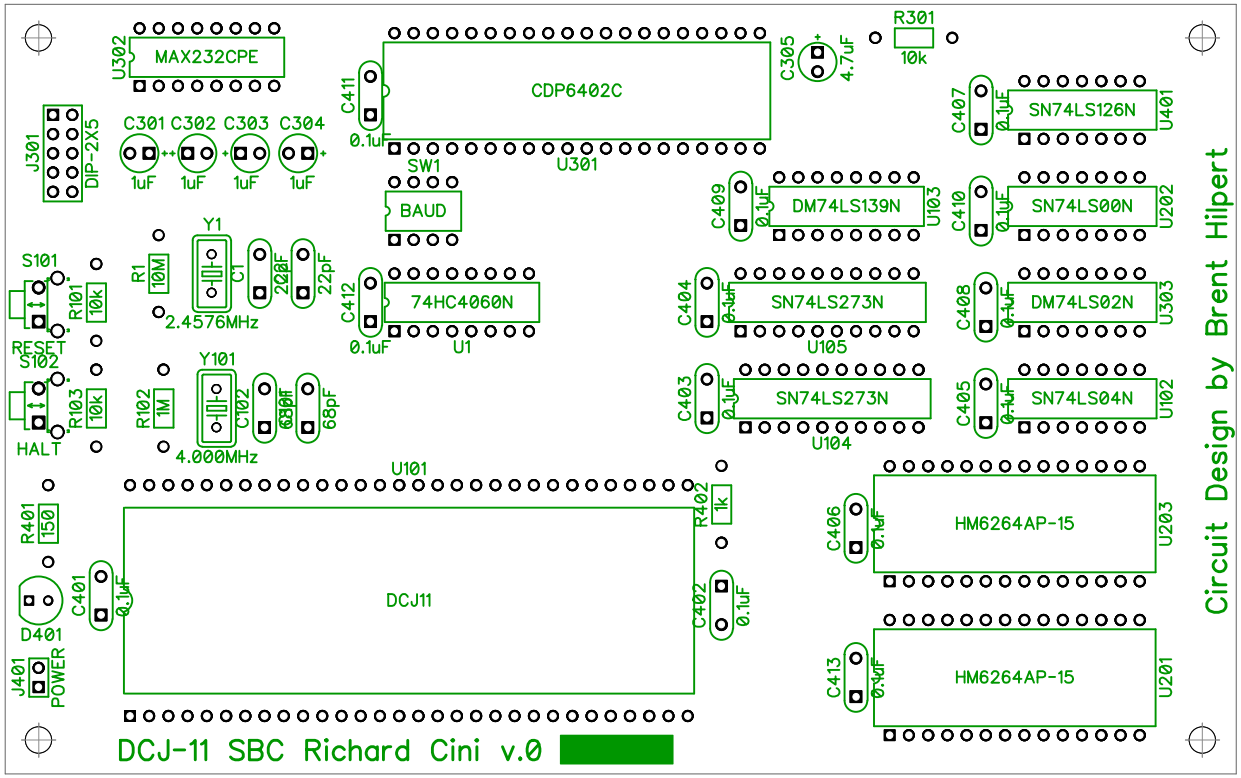
U302
MAX232CPE
J301
DIP-2X5
C301 C302 C303 C304
1uF 1uF 1uF 1uF
C411
0.1uF
CDP6402C
U301
C305
4.7uF
R301
10k
C407
0.1uF
SN74LS126N
U401
SW1
BAUD
C409
0.1uF
DM74LS139N
U103
C410
0.1uF
SN74LS00N
U202
S101
R101
10k
RESET
R1
10M
Y1
C1
20pF
22pF
2.4576MHz
C412
0.1uF
74HC4060N
U1
C404
0.1uF
SN74LS273N
U105
C408
0.1uF
DM74LS02N
U303
S102
R103
10k
HALT
R102
1M
Y101
C102
30pF
68pF
4.000MHz
C403
0.1uF
SN74LS273N
U104
C405
0.1uF
SN74LS04N
U102
U101
R401
150
D401
J401
POWER
C401
0.1uF
DCJ11
R402
1k
C402
0.1uF
C406
0.1uF
HM6264AP-15
U203
C413
0.1uF
HM6264AP-15
U201
DCJ-11 SBC Richard Cini v.0
Circuit Design by Brent Hilpert