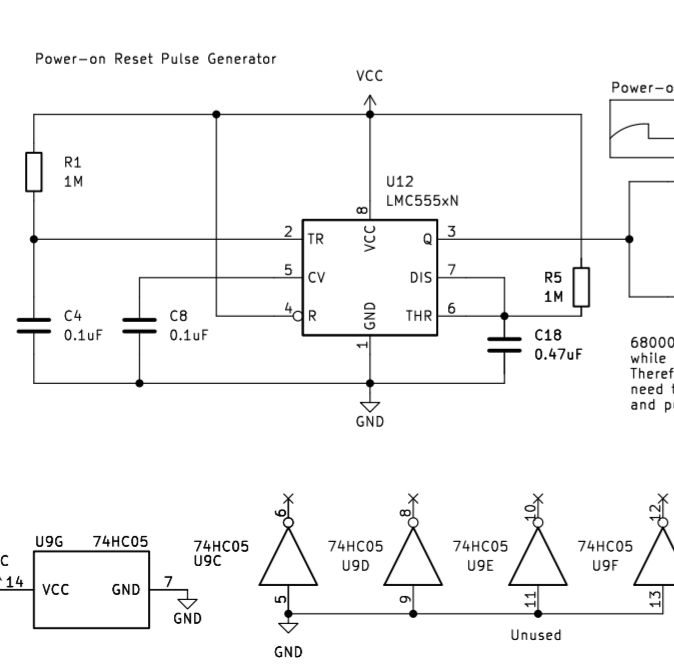
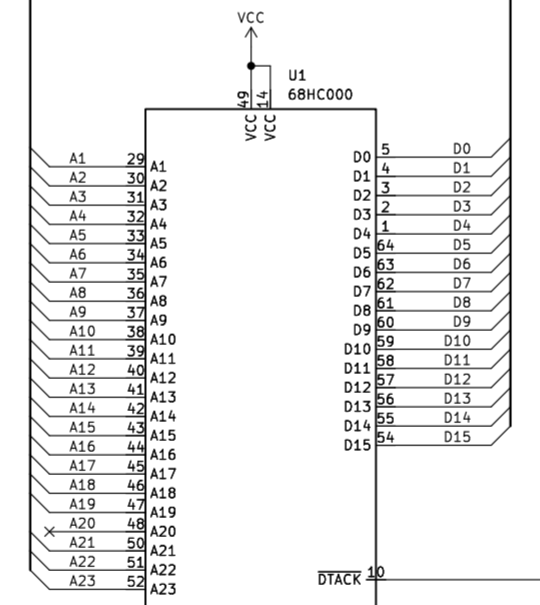
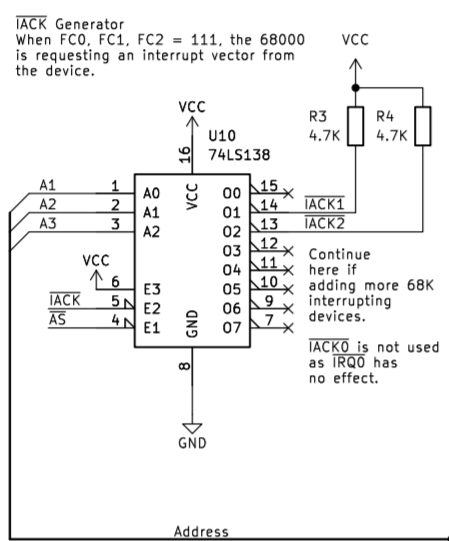
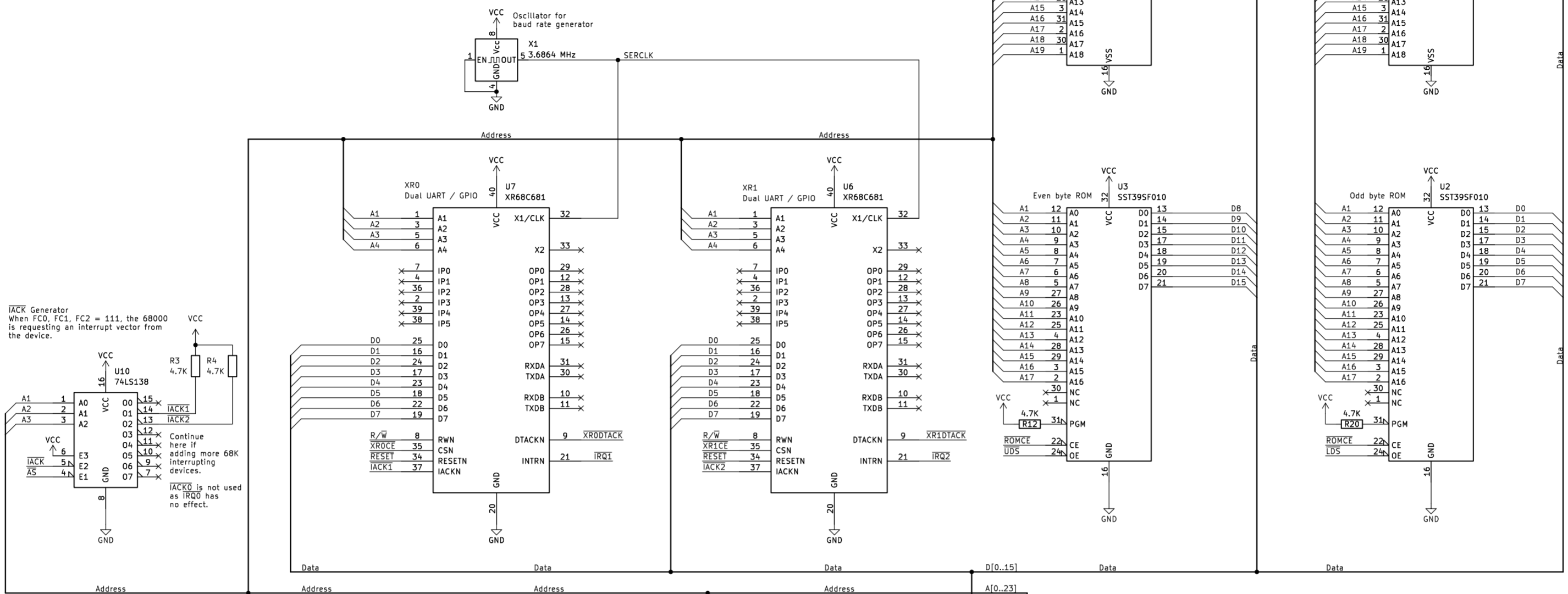
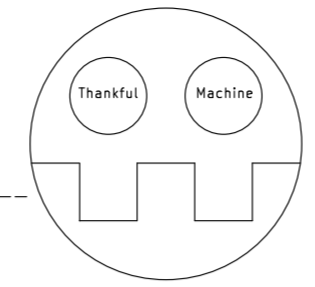


This might be incomplete or incorrect. You might blow up your precious 68K. Be careful.

Have fun!



68000 may drive the reset pins low while executing the reset instruction. Therefore, reset signal generators need to be open-drain/open-collector and pulled up.

GAL22V10
ADDRDECO

A23 A22 A21 A5 FC0 FC1 FC2 XR0DTACK XR1DTACK NC NC GND
NC NC NC NC DTACK IACK XRICE XROCE RAMCE ROMCE VCC

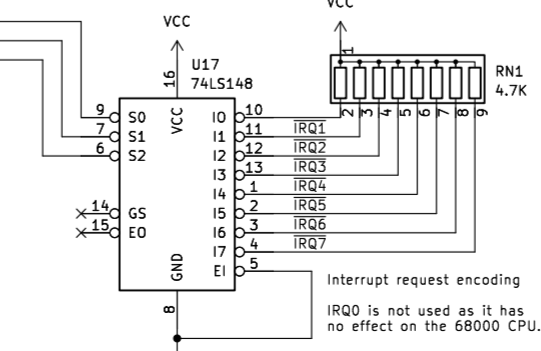
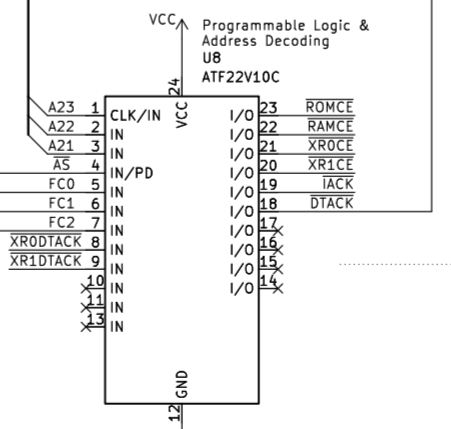
/IACK = FC0 * FC1 * FC2
/RAMCE = A23 * /A22 * /AS * IACK
/ROMCE = /A23 * /A22 * /AS * IACK
/XROCE = /A23 * A22 * /A21 * /AS * IACK
/XRICE = /A23 * A22 * A21 * /AS * IACK
DTACK = XR0DTACK * XR1DTACK * /ROMCE * /RAMCE

DESCRIPTION
Address decoder for M68000-based computer.
Incomplete decoding, many areas mirrored.

Programming:
% galasm = addrdeco.pld
% minipro --device "ATF22V10C(UES)" --write addrdeco.jed

Address Space:
00000000 ROM
00400000 XRO
00600000 XRI
00800000 RAM

DTACK:
Memories are assumed to be fast enough to have "instant" or "grounded" DTACK. The 68681 serial chips respond accordingly.



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Sheet: /
File: Bebop68k.kicad_sch
Title: Bebop 68000

Size: A2 Date: 2023-08-20
KiCad E.D.A. kicad 7.0.7

Rev: 2
Id: 1/1