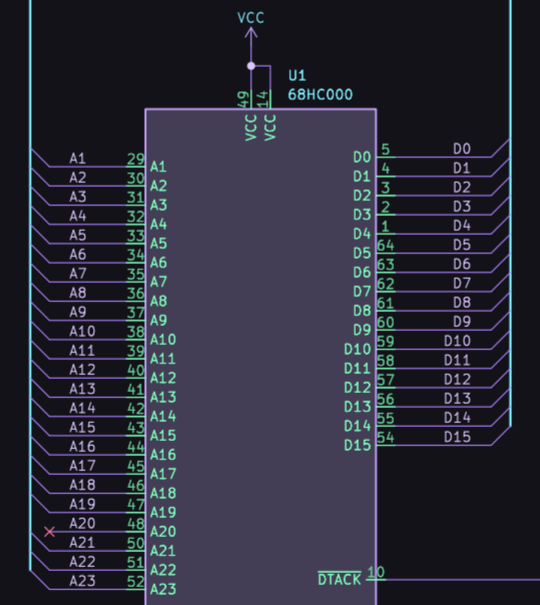
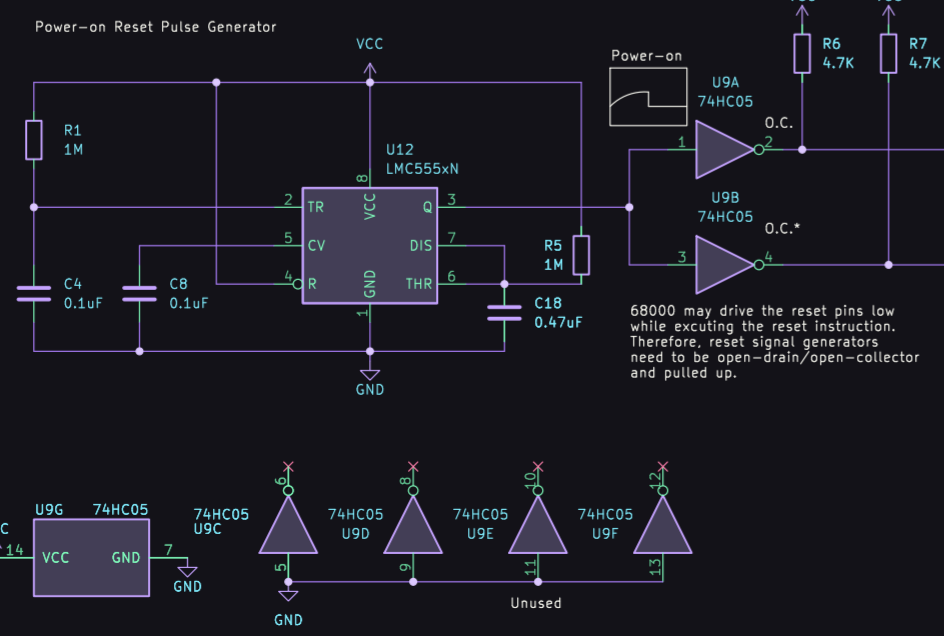
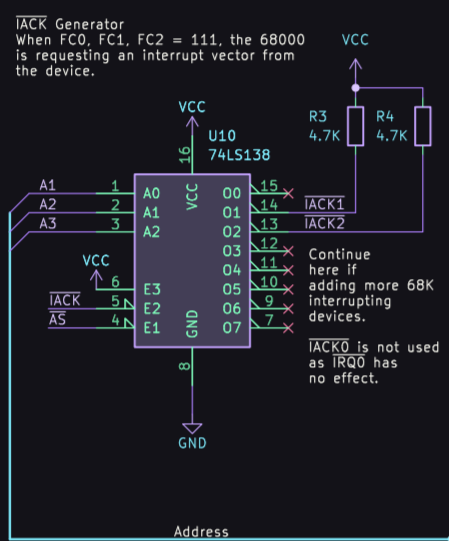
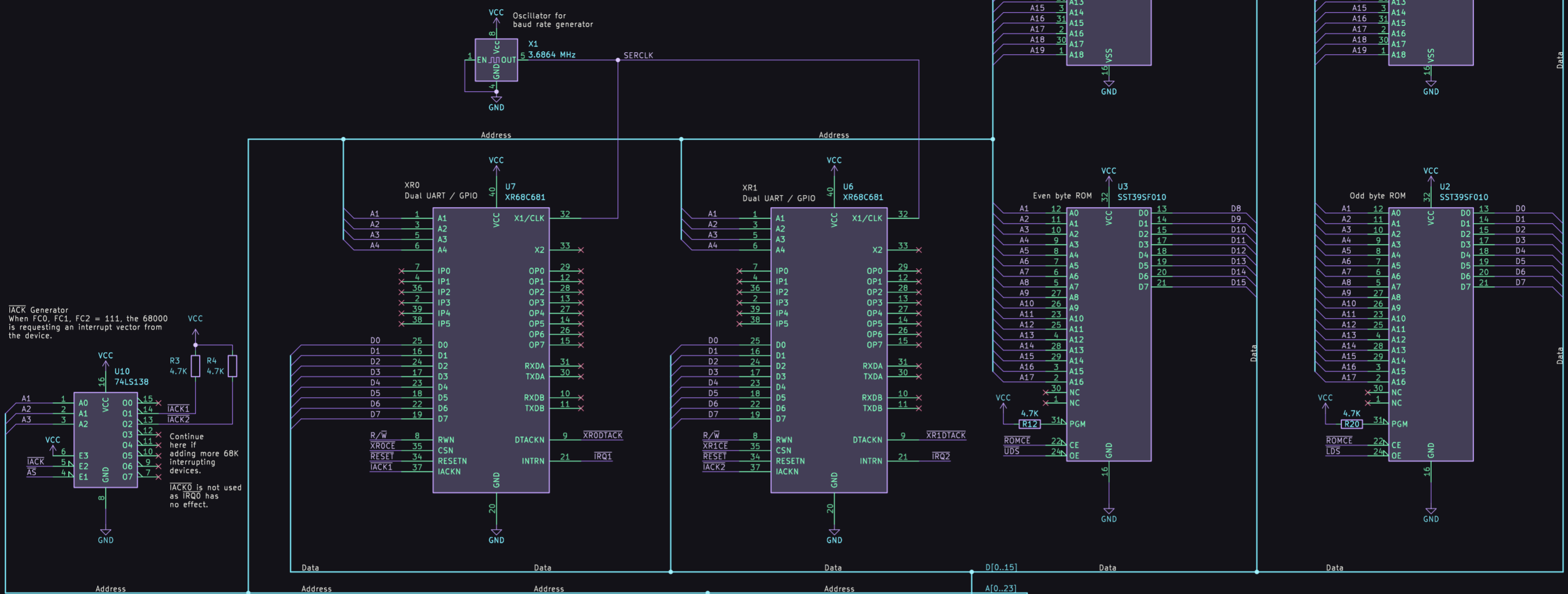
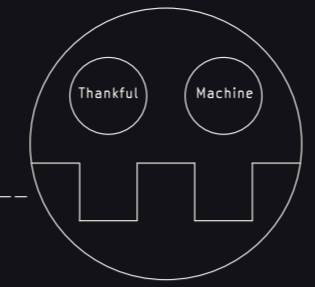


This might be incomplete or incorrect. You might blow up your precious 68K. Be careful.

Have fun!



GAL22V10
ADDRDECO

A23 A22 A21 A5 FC0 FC1 FC2 XRD/TACK XRI/TACK NC NC NC
NC NC NC NC DTACK IACK XRIICE XROCE RAMCE ROMCE VCC

$IACK = FC0 \cdot FC1 \cdot FC2$
 $/RAMCE = A23 \cdot /AS \cdot IACK$
 $/ROMCE = /A23 \cdot /A22 \cdot /AS \cdot IACK$
 $XROCE = /A23 \cdot A22 \cdot /A21 \cdot /AS \cdot IACK$
 $XRIICE = /A23 \cdot A22 \cdot A21 \cdot /AS \cdot IACK$
 $DTACK = XRD/TACK \cdot XRI/TACK \cdot /ROCE \cdot /RAMCE$

DESCRIPTION
Address decoder for M68000-based computer.
Incomplete decoding, many areas mirrored.

Programming:
% galasm -w addrdeco.pld
% minipro -d device "ATF22V10C(UES)" -w write addrdeco.jed

Address Space:
00000000 ROM
00400000 XRO
00600000 XRI
00800000 RAM

DTACK:
Memories are assumed to be fast enough to have "instant" or "grounded" DTACK. The 68681 serial chips respond accordingly.

